

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of Applicants: Date: July 30, 2008
Beaman et al. Group Art Unit: 2829
Serial No.: 09/382,834 Examiner: V. P. Nguyen
Filed: August 25, 1999 Docket No.: YOR919930028US5
For: HIGH DENSITY INTEGRATED CIRCUIT APPARATUS, TEST PROBE AND
METHODS OF USE THEREOF

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUBMISSION OF FORMAL DRAWINGS

Sir:

Enclosed herewith are twelve (12) sheets of formal drawings (FIGS. 17-39) replacing the previously added informal drawings submitted with the Amendment of June 16, 2005, for the above-identified application.

Please charge any fee necessary to enter this paper and any previous paper to deposit account 09-0468.

Respectfully submitted,

By: /Daniel P Morris/
Dr. Daniel P. Morris, Esq.
Reg. No. 32,053
Phone No. (914) 945-3217

IBM Corporation
Intellectual Property Law Dept.
P. O. Box 218
Yorktown Heights, New York 10598